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FIG. 1A (PRIOR ART)

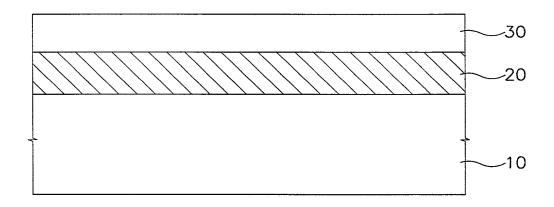


FIG. 1B (PRIOR ART)

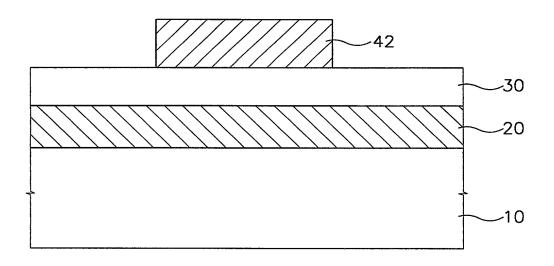


FIG. 1C (PRIOR ART)

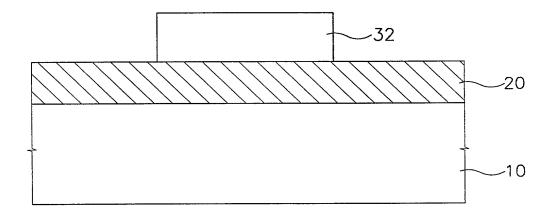
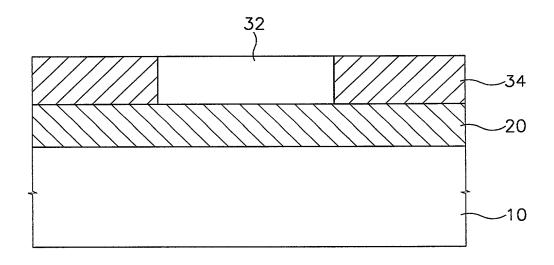
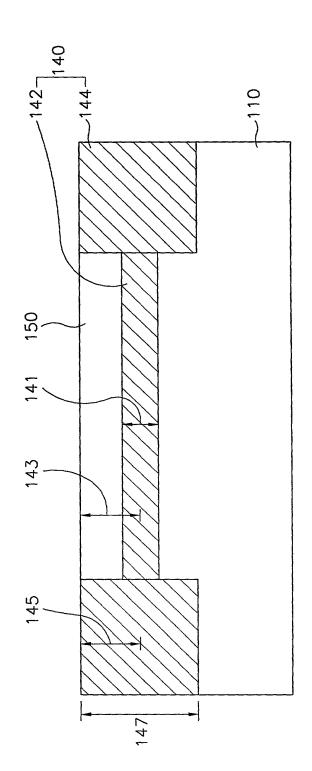


FIG. 1D (PRIOR ART)



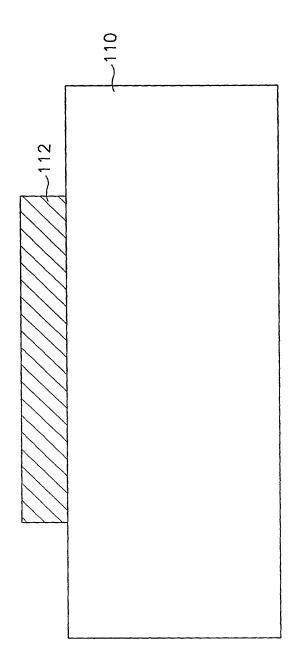
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SILICON-ON-INSULATOR (SOI) SUBSTRATE AND METHOD FOR MANUFACTURING THE SAME ALLON-INSULATOR (SOI) SUBSTRATE AND METHOD FOR MANUFACTURING THE SAME

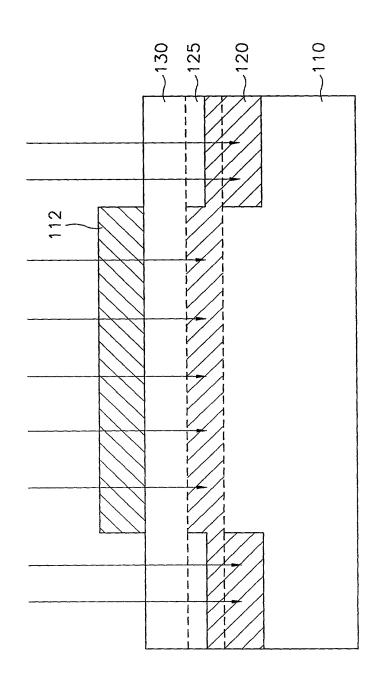
FIG. 2



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FIG. 3A





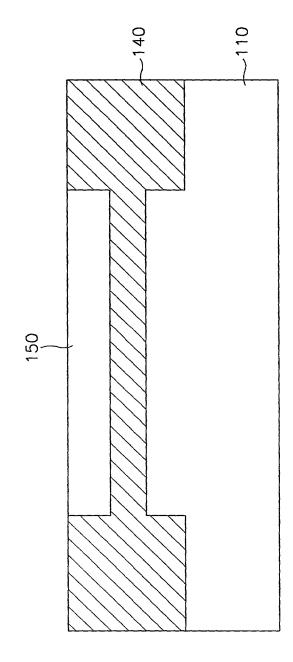


FIG. 4

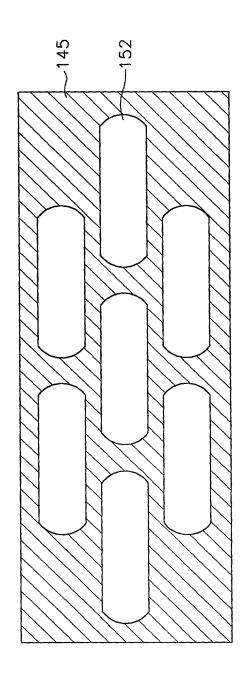


FIG. 5

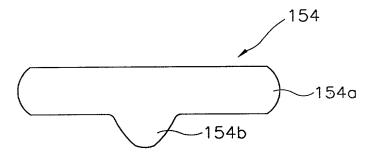
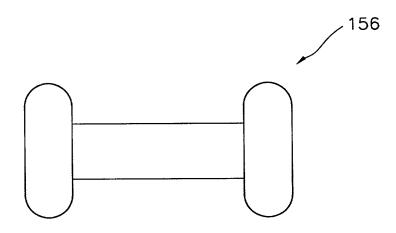


FIG. 6



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FIG. 7

